

## REMARKS

Claims 1-2, 4-8, 11, 12, 17, 19-25, 28-43, 51, 54, 55, 57, and 59-69 are now pending for consideration in the application. Claims 1, 17, 51, and 57 have been amended and claims 3, 9, 10, 13-16, 18, 26, 27, 44-50, 52, 53, 56, 58, and 70-73 have been canceled without prejudice to or disclaimer of the subject matter contained therein. The Applicants respectfully reserve the right to file a divisional application for any and all of the non-elected claims.

### Objection to Drawings

The drawings have been objected to as not showing certain claimed features of the invention. The Applicants respectfully traverse these objections based on at least the following remarks. The Applicants respectfully point out that the board of FIG 1 is described as, in some embodiments, being an adapter card, an add-in card, or a module, logging of an event is clearly illustrated in FIG 2, a component with a package such as a ball grid array or flip chip array package that is difficult to probe is illustrated, for example, in FIG 3, a connector that is difficult to probe, a surface mount connector, a metal can, etc. are illustrated, for example, in FIG 3. Withdrawal of the objection to the drawings is respectfully requested.

**Prior Art Rejections**

Claims 1-2, 4, 7-9, 11-14, 16-20, 23-29, 31, 51, and 53-69 have been rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent Number 5,923,099 (Bilir).

Claims 5-6, 21-22, 30 and 32-43 have been rejected under 35 U.S.C. 103(a) as being unpatentable over Bilir. The Applicants respectfully traverse these rejections based on the following remarks.

First, it is respectfully pointed out that Bilir does not disclose a content protection apparatus or system. Bilir discloses an intelligent backup power controller that performs graceful shut-down of a processing system based upon the loss of main AC power. No detection is made relating to a coupling condition that is an indication of whether a board and a device are coupled together as specifically recited in the independent claims of the present application. Instead, the uninterruptable power source 30 of Bilir is used to provide power to the processing system 10. The backup controller 50 detects a loss of AC power to the entire system to enable a switch to backup power, triggering a sequence of events to affect the graceful shutdown of the processing system 10. Bilir does not disclose or suggest at least features of the present invention as claimed of detecting a board coupling condition that is indicative of a coupling between a board and a device in order to perform a shutdown in response to the board coupling condition. At best, Bilir discloses a backup controller to shut down a system when AC power is not provided to the system. Therefore, for at least these reasons, the

Applicants respectfully traverse the prior art rejections. Withdrawal of these rejections is respectfully requested.

The Applicants respectfully request further prosecution on the merits for this application. The Applicants believe that this application is in condition for allowance, and early notification of the same is earnestly solicited. If there are any questions regarding the present application, the Examiner is invited to contact the undersigned attorney via telephone at 815-885-1390 or via email at [rob@intel.com](mailto:rob@intel.com).

Respectfully submitted,

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Date

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